

|   |   |       |            |
|---|---|-------|------------|
| <u>L40</u>  | l2 and l12  | 1     | <u>L40</u> |
| <u>L39</u>  | l2 and l11  | 0     | <u>L39</u> |
| <u>L38</u>  | l2 and l10  | 69    | <u>L38</u> |
| <u>L37</u>  | l2 and l9   | 14    | <u>L37</u> |
| <u>L36</u>  | l2 and l8   | 821   | <u>L36</u> |
| <u>L35</u>  | l21 and l14   | 0     | <u>L35</u> |
| <u>L34</u>  | l21 and l13   | 0     | <u>L34</u> |
| <u>L33</u>  | l21 and l12   | 0     | <u>L33</u> |
| <u>L32</u>  | l21 and l11   | 0     | <u>L32</u> |
| <u>L31</u>  | l21 and l10   | 28    | <u>L31</u> |
| <u>L30</u>  | l21 and l9  | 5     | <u>L30</u> |
| <u>L29</u>  | l21 and l8  | 276   | <u>L29</u> |
| <u>L28</u>  | l6 and (l8 or l9 or l10 or l11 or l12 or l13 or l14)  | 458   | <u>L28</u> |
| <u>L27</u>  | l7 and l14  | 0     | <u>L27</u> |
| <u>L26</u>  | l7 and l13  | 0     | <u>L26</u> |
| <u>L25</u>  | l7 and l12  | 0     | <u>L25</u> |
| <u>L24</u>  | l7 and l11  | 0     | <u>L24</u> |
| <u>L23</u>  | l7 and l10  | 28    | <u>L23</u> |
| <u>L22</u>  | l7 and l9   | 5     | <u>L22</u> |
| <u>L21</u>  | l7 and l8   | 276   | <u>L21</u> |
| <u>L20</u>  | l2 and l17  | 1     | <u>L20</u> |
| <u>L19</u>  | l2 and l15  | 29    | <u>L19</u> |
| <u>L18</u>  | l7 and l16  | 1     | <u>L18</u> |
| <u>L17</u>  | l7 and l15  | 1     | <u>L17</u> |
| <u>L16</u>  | (credit or smart) near3 card\$1   | 93939 | <u>L16</u> |
| <u>L15</u>  | smart near3 card\$1   | 35522 | <u>L15</u> |
| <i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>                          |   |       |            |
| <u>L14</u>  | (713/164)[CCLS]   | 385   | <u>L14</u> |
| <u>L13</u>  | (725/6)![CCLS]  | 78    | <u>L13</u> |
| <u>L12</u>  | (902/26)![CCLS]   | 178   | <u>L12</u> |
| <u>L11</u>  | (705/64)[CCLS]  | 774   | <u>L11</u> |
| <u>L10</u>  | (712/32-38)[CCLS]   | 1456  | <u>L10</u> |
| <u>L9</u>   | (712/201-203)[CCLS]   | 419   | <u>L9</u>  |
| <u>L8</u>   | (712/2-300)[CCLS]   | 13008 | <u>L8</u>  |
| <i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i> |   |       |            |
| <u>L7</u>   | L6 and stack\$3   | 587   | <u>L7</u>  |
| <u>L6</u>   | L2 and (tempor\$6 or auxiliar\$4 or architectur\$4) near4 register\$1   | 1323  | <u>L6</u>  |
| <u>L5</u>   | L3 and stack\$3   | 24    | <u>L5</u>  |
| <u>L4</u>   | L3 near55 stack\$3  | 3     | <u>L4</u>  |
| <u>L3</u>   | L2 near55 (tempor\$6 or auxiliar\$4 or architectur\$4) near4 register\$1  | 80    | <u>L3</u>  |
| <u>L2</u>   | (correspond\$5 or respect\$5 or map\$6 or associat\$4 or correlat\$4) near12 bit\$1<br>near12 register\$1 near4 (bank\$1 or subgroup\$4 or group\$4 or subset\$1 or | 4865  | <u>L2</u>  |

set\$1 or file\$1)  
(correspond\$5 or respect\$5 or map\$6 or associat\$4 or corelat\$4) near12 bit\$1  
L1 near12 register\$1 near4 (bank\$1 or subgroup\$4 or group\$4 or subset\$1 or  
set\$1 or file\$1) 4812 L1

END OF SEARCH HISTORY